

# $Diamond_X HSI_X$

### Scalable, Cost-Efficient Solution for High Performance SerDes Test



The HSI1<sub>x</sub> is optimized for testing clock embedded and clock-forwarded serial interface commonly found in modern mobile, consumer, industrial and automotive electronics. These ports connect modern cameras, displays, storage and applications processors to enable high bandwidth, low power consumption, and low EMI.

### Highlights

- Physical layer testing with built in PRBS BERTTX/RX
- BIST/DFT testing using high bandwidth drive/compare memory
- Protocol level mixed-signal testing using deep send pattern memory

### **Features**

- Test of high speed serial ports with data rates up to 12.8 Gbps, such as HDMI, MIPI, JESD204, PCIexpress, SATA, EPD, Vby1 and USB3
- 32 differential TX channels
- 24 differential RX channels
- Hardware clock data recovery per lane with flexible BERT sync
- Deep source memory
- Flexible loopback modes including closed loop BERT
- Calibrated jitter injection on all lanes
- Flexible pre-emphasis and equalization





Consumer



Flat Panel Display



IoT/IoV & Optoelectronics



Industrial & Medical



MCU



Mobility

- SerDes and LVDS/MIPI
- 32 differential TX channels
- 24 RX Differential Channels

- 12.8 Gb Data Rate
- 250M TX Vector Memory Jitter Injection
- Eye Mask, PRBS

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## Scalable, Cost-Efficient Solution for High Performance SerDes Test

### **Specifications**

Data Rate <ul> <li>Data rate range</li> <li>Eraguancy resolution</li> </ul>	400 Mbps to 12.8 Gpbs 1 KHz
<ul> <li>Frequency resolution</li> </ul>	I KHZ
<ul> <li>Transmitter</li> <li>Number of differential TX channels</li> <li>AC output differential impedance</li> <li>Differential voltage swing</li> <li>Pre-emphasis range</li> <li>Jitter Frequency range</li> <li>Max injected deterministic</li> </ul>	32 100 Ω 40 mV to 950 mV +/-300 mV (Typical) 0.1 KHz to 20 MHz 1.4 ns
jitter, peak-peak • Maximum RMS random jitter injection	0.1 UI
<ul> <li>Receivers</li> <li>Number of differential RX channels</li> <li>AC input differential impedance</li> <li>Input differential voltage range</li> <li>Input single ended range</li> <li>Programmable comparator threshold voltage</li> <li>Extracted clock domains</li> <li>Equalization gain</li> </ul>	24 100 Ω 25 mV to 1.4 V 360 mV to 1070 mV +/-0.5 V 2 1 to 2.4
<ul> <li>Pattern Specifications</li> <li>Built in patterns</li> <li>Pattern segment size</li> <li>Total memory space for TX</li> <li>Total memory space for RX</li> <li>Sequence control</li> <li>Maximum loop count per sequencer slot</li> <li>Triggering</li> </ul>	K28.5, PRBS 5, 7, 9 11, 13, 15, 21, 23, 31, and invert, toggle, all ones, all zeros 512 bits – 64 Kbits 8 Gb 2 Mb Loop infinite, Loop on count, Play to end 2**16 - 1 External or internal

All specifications are subject to change without notification and are for reference only. For detailed performance specifications, please contact Cohu.

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www.cohu.com/diamondx-instrumentation www.cohu.com/ate

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