

DIAMOND SERIES DPINg6-64



The Value Standard for Digital Testing

The DPINg6 is an excellent high-value solution for testing digital and mixed-signal devices in the consumer, automotive and wireless markets.

- Flexible timing
- Reconfigurable pattern memory
- Deep capture memory
- High-precision PMU
- Built-in time measurement
- Super voltage
- Comprehensive software tools

The DPINg6 instrument for the Diamond Series offers full digital performance in a compact size at a very low price.

Features:

- 96 digital channels on a single instrument provide the high density and full set of features required for low-cost multisite testing.
- Per-pin PMUs can perform precision measurements or quick go/no-go tests for the fastest production test times.
- Up to 64M reconfigurable pattern memory shared between parallel vectors and scan vectors for the ultimate memory flexibility.
- 32M per pin capture memory can be used for capturing non-deterministic digital data from ADCs or for capturing functional failures.

DIAMOND SERIES DPING6-64

Flexible Timing

Edges and formats are per-pin programmable, and format switching and edge time set switching are allowed on-the-fly. The Dping6 supports up to 256 different format sets and 256 different period and edge time sets.

Reconfigurable Pattern Memory

The pattern memory of the Dping6 can be divided between parallel vectors and scan vectors. Division of the memory is determined automatically at pattern load time. The adaptability of the reconfigurable pattern memory allows the user to make the most efficient use of the pattern resources. ASCII pattern format is used for maximum compatibility with EDA software environments.

Deep Capture Memory

The 32M deep capture memory of the Dping6 instrument can be used for the testing of ADCs and other non-deterministic data sets, or it can be used to capture functional failures for online debugging or offline analysis in the EDA environment. All channels on the Dping6 have the ability to capture data.

High-Precision PMU

Each channel of the Dping6 has its own high-speed PMU. Levels and limits are programmable on a per-channel basis. The per-pin PMU feature enables extremely fast current and voltage measurements for production test.

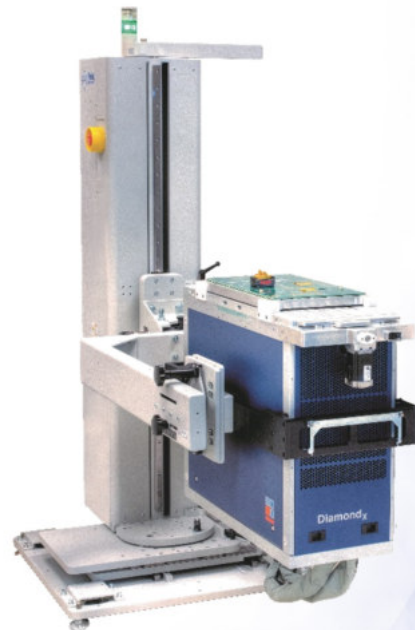
A precision ADC is also available on each Dping6 instrument. The ADC can be connected to any channel through a convenient on-board matrix. Not only does the PMU architecture of the Dping6 promote fast test times, but the per-pin PMUs and built-in ADC matrix ensure that the user's load board remains simple and clean.

Built-in Time Measurement

The Dping6 has built-in time measurement capabilities. A frequency counter and an interval counter are part of the Dping6 instrument controller. An internal matrix allows routing to the frequency counter and interval counter. The high comparator of each signal determines the trigger level. Many parameters can be measured, such as pulse width, period, frequency and time differences.

Super Voltage

Many devices require a super voltage for programming embedded flash or entering test mode. The Dping6 supplies a pattern-controlled, per-pin super voltage of up to 12 V for this purpose. This feature eliminates the trouble and expense of designing external load board circuitry to accomplish DUT programming.



DIAMOND SERIES DPIN96-64

1 Digital Pin Configurations

- 1.1 Channels Per Instrument: 96
- 1.2 Maximum Data Rate: 100 Mbps
- 1.3 Maximum Clock Rate: 200 MHz
- 1.4 Instruction Rate: 8 kHz to 100 MHz

2 Digital Pin Driver

- 2.1 Vih, Vil Range: -1 V to +6 V
- 2.2 DC Level Accuracy: ± 15 mV
- 2.3 Output Impedance: 50 ohms ± 5 ohms typical
- 2.4 Edges Per Cycle: 4
- 2.5 Edge Rise/Fall Time (1 V): 1 ns typical
- 2.6 Edge Rise/Fall Time (3 V): 1 ns typical
- 2.7 Minimum Pulse Width: 2.5 ns
- 2.8 Drive Level Programming: Independent high & low per pin
- 2.9 Minimum Voltage Swing: 200 mV
- 2.10 Super Voltage Per Pin: 12 V
- 2.11 Pattern Trigger Capability: Supported

3 Digital Pin Comparator

- 3.1 Compare Levels: Independent high & low per pin
- 3.2 Compare Threshold:
 - Range: -1 V to +6 V
 - Accuracy: ± 15 mV

4 Timing Generator

- 4.1 Architecture: Per pin
- 4.2 Number of Edges: 4 edges per pin
- 4.3 Format Sets: 256 per time domain
- 4.4 Time Sets: 256 (period and edge)

5 Pattern Source Memory

- 5.1 Parallel Vectors:
 - Max Depth:
 - Reconfigurable 64M vectors
 - Bits Per Pin: 3
- 5.2 Scan Vectors Maximum Depth Per Board Dping6-64:
 - 4608M x 2 chains
 - 2304M x 4 chains
 - 1152M x 8 chains
 - 576M x 16 chains
 - 288M x 32 chains

6 Pattern Capture Memory

- 6.1 Modes: Capture errors or data
- 6.2 Max Depth: 32M vectors

7 Frequency Counter

- 7.1 Maximum Frequency: 200 MHz
- 7.2 Resolution: 32 bit
- 7.3 Switchable to any pin: Yes

8 Parametric Measurement Unit (PMU)

- 8.1 Architecture: Per Pin
- 8.2 Force Voltage Range: 8V (-1 V to +7 V) 12 V (-1 V to +11 V)
- 8.3 Force Voltage Accuracy: ± 15 mV / ± 25 mV
- 8.4 Measure Voltage Range: 8 V (-1 V to +7 V)
- 8.5 Measure Voltage Accuracy: ± 15 mV
- 8.6 Force Current Range:
 - 32 mA, 8 mA, 2 mA, 512 μ A, 128 μ A, 32 μ A, 8 μ A, 2 μ A
- 8.7 Force Current Accuracy: $\pm(2\%$ of range + 80 nA)

All specifications are subject to change without notice.

